

Enroll No

Q.P.Code
UT 3132

Unit Test -II (2025-26)

S.Y. B.Tech.- Robotics & Automation Engineering

Course Code: RA207

Course Name: Analog and Digital Electronics

Day & Date: Friday 19/09/2025

Time: 11:45 To 12:45

Max Marks- 25

Instructions: 1) All Questions are Compulsory

- 2) Figures to the right indicate maximum marks
- 3) Assume suitable data if not given
- 4) Use of non-programmable calculator is allowed

		Marks	COs	BT Level
Q.1	A Explain following.	6	CO3	4
	i. Inverting Operational Amplifier (3 marks)			
	ii. Non-Inverting Operational Amplifier (3 marks)			
	B Explain any THREE of the following with the help of suitable diagram.	9	CO3	5
	i. Summing Amplifier (3 Marks).			
	ii. Differentiator (3 Marks).			
	iii. Integrator (3 Marks).			
	iv. Difference Amplifier (3 Marks).			
	v. Inverting Comparator (3 Marks).			
	vi. Non-Inverting Comparator (3 Marks).			
Q.2	A Convert following	4	CO4	3
	i. 1100110110 (binary) to octal (2 Marks).			
	ii. 101101 (binary) to decimal (2 Marks).			
	B Explain following Logic Gates with truth table and Logic Symbols for two inputs.	6	CO4	3
	i. AND (2 Marks).			
	ii. NOR (2 Marks).			
	iii. NAND (2 Marks).			

